

Ethernet-APL Test Guide

Device Under Test Setup:

- It is expected that all tests are performed with PHY communication abilities disabled. This is achieved by disabling Auto-Negotiation and setting the PHY to SLAVE mode. Regardless of the PHY state, each data line of the port under test shall be terminated with a 50 Ohm resistance behind a 1 μ F series capacitor in the Telebyte Probe.
- Enter the Power Class for the Device Under Test (Class A, B or C) into the test automation software.

Expected Results (Pass/Fail Criteria):

Step	Status	Description
3	PASS	I_{PL} never exceeds $I_{PL(MAX)}$ for the port power class (Class A = 55.56mA, Class B = 115mA & Class C = 95mA)
3	FAIL	I_{PL} exceeds $I_{PL(MAX)}$ for the port power class at least once

Notes:

References:

- [1] APL Port Profile 1.2 Section 5.4
- [2] Methods Annex – Sampling with Digital Multimeter
- [3] Methods Annex – Disabling PHY
- [4] Methods Annex – Power Supply Voltage Sensing